45 PTO	Subclas:	ASSIFICATIO
09/7036 11/02/8	Class	ISSUECLA
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APPLICATION NO.

09/703845

U.S. UTILITY Patent Application PATENT DATE

SCANNED

EXAMINER ~ CLASS SUBCLASS ART UNIT CONT/PRIOR 324

Matsumoto Toshiyuki Yakabe Masami Hirota Yoshihiro

2829 769

HOLLINGTON

Capacitance measurement method of micro structures of integrated circuits

	ISSUING C	LASSIFICATION
ORIGINAL		CROSS REFERENCE(S)
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TERMINAL		DRAWINGS		CLAIMS	S ALLOWED
DISCLAIMER	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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not extend beyond the expiration date of U.S Patent. No.				ISSU	JE FEE ACTION
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this patent have been disclaimed.	(Legal Instruments Examiner) (Date		(Date)		
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